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In re: application of: Dirk Reese et al.

Application Number: 10/786,370

Filed: February 24, 2004

Confirmation Number: 2811

Title: OVER-VOLTAGE PROTECTION OF INTEGRATED CIRCUITS

Atty Docket Number: 015114-067500US JMZ/djb

Being faxed to the Office of Initial Patent Examination's Filing Receipt Corrections at  
facsimile number 1-703-746-9195 are the following documents:

This PTO/SB/97 Certificate of Transmission (1 page);  
Request for Corrected Filing Receipt (1 page);  
Marked up copy of page 1 of filing receipt (1 page); and  
Copy of express mail label and return postcard (1 page)

Number of pages being transmitted: 4

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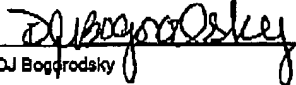
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**PATENT**  
Attorney Docket No.: 015114-067500US  
Client Reference A1060

TOWNSEND and TOWNSEND and CREW LLP

By:   
DJ Bogrodsky

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Dirk Reese et al.

Application No.: 10/786,370

Filed: February 24, 2004

For: OVER-VOLTAGE PROTECTION OF  
INTEGRATED CIRCUIT I/O PINS

Customer No.: 26059

Confirmation No.: 3403

Examiner: Unassigned

Art Unit: 2811

**REQUEST FOR CORRECTED FILING RECEIPT**

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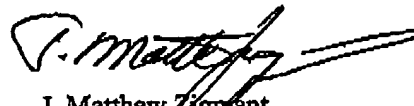
Sir:

Attached is a copy of the filing receipt received from the Patent and Trademark Office in the above-noted application for which issuance of a corrected filing receipt is respectfully requested.

The filing date on the filing receipt is incorrect. The filing receipt indicates 02/25/2004 as the filing date. Enclosed is a copy of the express mail label and return postcard. As can be seen, the post office written and stamped date is 02/24/2004, and the return postcard with the USPTO sticker has the correct date of 02/24/2004.

The correction requested is not due to any error by applicants and applicants believe no fee is due.

Respectfully submitted,

  
J. Matthew Ziegant  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE RECD	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/786,370	<del>02/25/2004</del> 02/24/2004	2811	770	015114-067500US	10	20	3

CONFIRMATION NO. 3403

26059  
 TOWNSEND AND TOWNSEND AND CREW LLP/ 015114  
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## FILING RECEIPT



\*OC000000012702705\*

Date Mailed: 05/18/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Assignment For Published Patent Application

Altera Corporation, San Jose, CA;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted: 05/18/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No



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## UNITED STATES PATENT AND TRADEMARK OFFICE

 Mailing Date: **February 24, 2004** Express Mail Label No.: **EV 346 925 323 US**  
 File No.: **015114-067500US** Attorney: **JMZ/sgs**  
 Serial No.: **Unassigned** Filed: **Herewith**  
 Inventors: **Dirk Reese et al.**

 Title: **OVER-VOLTAGE PROTECTION OF INTEGRATED CIRCUIT I/O PINS**

1. Application Data Sheet (5 pages);
2. PTO/SB/05 Application Transmittal (1 page);
3. PTO/SB/17 Fee Transmittal (1 page submitted in duplicate);
4. PTO/SB/35 Non-Publication Request (1 page);
5. PTO/SB/01A Declaration (2 pages);
6. Non-Provisional Application 1 title page, 12 pages of specification, 4 pages of claims, 1 page of abstract, 10 pages of informal drawings;
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